

VLSI Final Project Report

Testing & Fault Tolerance in Digital Systems

John Adams && Mary Mouro | VLSI Testing | April 30, 2019

## compile instructions

1. Download and unzip our project named “vlsi\_Final\_Project”from canvas
2. Run command $ cd vlsi\_Final\_Project
3. Run command $ pip3 install sympy
4. Run command $ sudo apt-get install libz-dev
5. Run command $ cd vender/minisat
6. Run command $ make config prefix=$PREFIX
7. Run command $ sudo make install
8. Run command $ mv build/dynamic/bin/minisat $PATH TO vlsi\_Final\_Project/bin
9. move benchmark (.ckt files) to the project vlsi\_Fault\_TG/benchmarks
10. cd to project directory vlsi\_Fault\_TG/src
11. run command "python3 main.py"

### NOTES:

Tested on linux Ubuntu 18.04 LTS

Minisat set solver has some dependencies that need to be installed, other project dependencies are listed below

### DEPENDENCIES:

sympy

libz-dev

python3

g++

## code structure

You might like the photo on the cover page as much as we do, but if it’s not ideal for your report, it’s easy to replace it with your own.

Just delete the placeholder picture. Then, on the Insert tab, click Picture to select one from your files.

## Results

# Summary

To replace the placeholder text on this page, you can just select it all and then start typing. But don’t do that just yet!

First check out a few tips to help you quickly format your report. You might be amazed at how easy it is.

* Need a heading? On the Home tab, in the Styles gallery, just click the heading style you want.
* Notice other styles in that gallery as well, such as for a quote, a numbered list, or a bulleted list like this one.

For best results when selecting text to copy or edit, don’t include space to the left or right of the characters in your selection.